

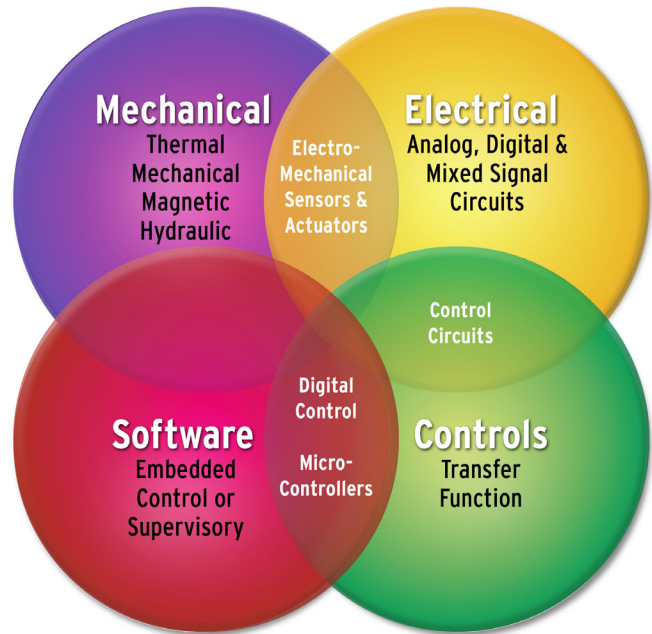
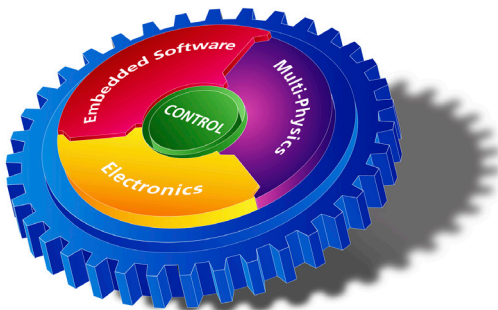
Leveraging Standards for Powerful System Modeling

Rather than create yet another proprietary modeling format, the SystemVision environment leverages existing, industry-standard modeling languages such as VHDL-AMS, the IEEE standard for modeling a wide variety of electronic, multi-physics and embedded software components at multiple levels of design abstraction. It also supports ModelSim®-compatible libraries; SPICE format models; and C/C++, SystemC, and Verilog-AMS language modeling to provide exceptional modeling flexibility. Entire systems may be modeled and analyzed in an individual language or languages can be fluently mixed within a single design.

Flexible Modeling and Analysis

The SystemVision environment supports any level of component and system description. Levels of abstraction span the more general behavioral, signal-flow (s-domain) and sampled-data (z-domain) descriptions, to the more specific component/device level implementations. A single system model may contain multi-level descriptions, where one portion of the model may be behaviorally-based and another physics-based.

The SystemVision single-kernel simulation engine accepts multi-language system and circuit descriptions, generates data in a technology-aware format, and ensures accurate mixed-signal and multi-domain simulation results. It includes graphical design entry, VHDL-AMS model and library development tools, leading-edge simulation technology, and powerful waveform viewing and data analysis tools.



Integration with Leading Industry Tools

The SystemVision design environment leverages the SystemVision SVX technology to link with other industry-leading tools, bringing together best-in-class system modeling, analysis, and test capabilities in a single development environment. Integrating the SystemVision design environment with Simulink® combines mechatronic system hardware design and analysis with control algorithm development. Integration with the National Instruments LabVIEW™ graphical test system programming environment enables concurrent system and test-program development from the beginning to the end of the design flow. With these and similar integrations, the SystemVision environment is an essential tool for developing and testing complex mechatronic systems.

System Requirements

Operating Systems:

- Windows XP Pro (SP1, SP2); Windows 7

Minimum System Requirements:

- 1.0 GHz clock
- 512 MB RAM
- 3 GB disk space

For additional information please visit us at www.mentor.com/systemmodeling

Copyright ©2010 Mentor Graphics Corporation. Mentor products and processes are registered and registered trademarks of Mentor Graphics Corporation. All other trademarks mentioned in this document are trademarks of their respective owners.

Corporate Headquarters
Mentor Graphics Corporation
8005 SW Boeckman Road
Wilsonville, OR 97070-7777
Phone: 503.685.7000
Fax: 503.685.1204

Sales and Product Information
Phone: 800.547.3000
sales_info@mentor.com

Silicon Valley
Mentor Graphics Corporation
46871 Bayside Parkway
Fremont, CA 94538 USA
Phone: 510.354.7400
Fax: 510.354.7467

North American Support Center
Phone: 800.547.4303

Europe
Mentor Graphics
Deutschland GmbH
Arnulfstrasse 201
80634 Munich
Germany
Phone: +49.89.57096.0
Fax: +49.89.57096.400

Pacific Rim
Mentor Graphics (Taiwan)
Room 1001, 10F
International Trade Building
No. 333, Section 1, Keelung Road
Taipei, Taiwan, ROC
Phone: 886.2.87252000
Fax: 886.2.27576027

Japan
Mentor Graphics Japan Co., Ltd.
Gotenyama Garden
7-35, Kita-Shinagawa 4-chome
Shinagawa-Ku, Tokyo 140-0001
Japan
Phone: +81.3.5488.3033
Fax: +81.3.5488.3004

**Mentor
Graphics**