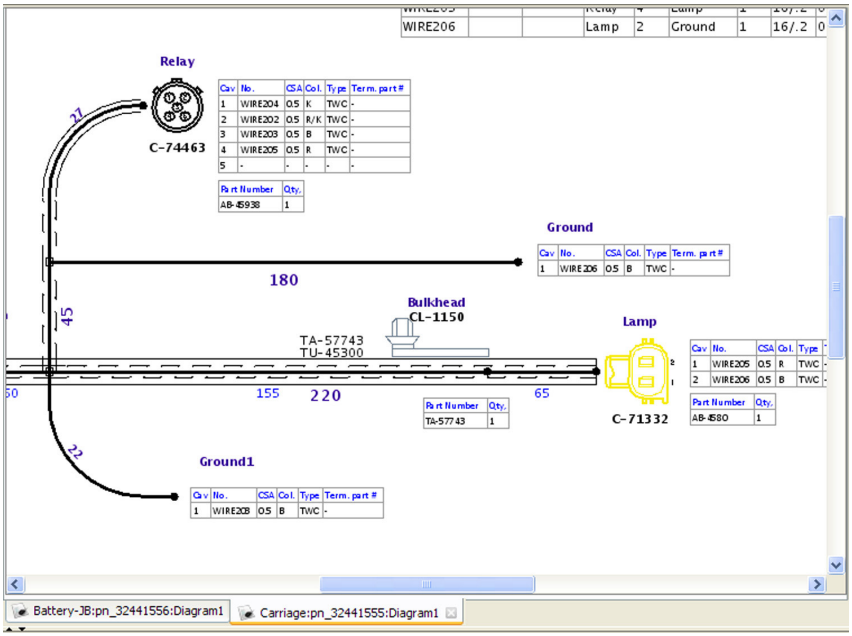
Electrical Systems Design and Wire Harness Engineering

D A T A S H E E T

VeSys® 2.0 Harness



Create fully detailed and engineered harness and formboard drawings.

Intuitive

VeSys 2.0 Harness provides a graphical environment for creating harness and formboard drawings. Its intuitive user interface makes harness design authoring easy. Built-in harness-engineering intelligence automates many design tasks. For example, connector tables are automatically populated as wires are added, and terminals are automatically selected and wire tables automatically generated. Furthermore, all entities have context-sensitive menus giving the user the modification options appropriate for each different type of component. These and many other facilities make harness design a simple and quick task.

Powerful

VeSys 2.0 Harness includes a powerful automated parts selector that automatically configures and selects terminals, seals, and wires for each connector, including allowances for add-ons and knock-offs, speeding design and eliminating a major source of problems found in the traditional design process.

Details including wire lengths and manufacturing reports are available instantly for viewing. New harness design data is validated prior to automatically generating the manufacturing documentation. The result is unsurpassed accuracy and productivity in harness design.

Diagram appearance – line weights, fonts, colors, symbols, borders, etc is fully customizable.

VeSys 2.0 Harness designs can be linked with VeSys 2.0 Design diagrams, enabling wires, connectors and splices from the circuit to automatically populate the

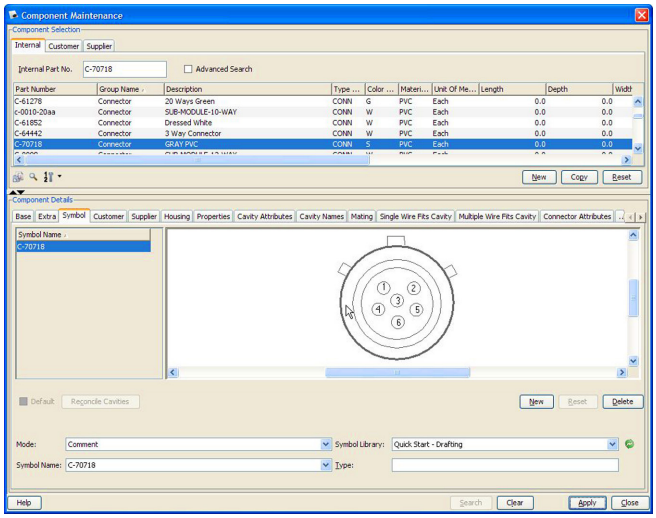
VeSys 2.0 Harness Highlights

- Rapid graphical layout and engineering of harness and formboard designs
- Automatic part selection of terminals, seals and connectors, etc.
- Automatic generation of harness manufacturing reports including BOMs and cutting lists
- Harnesses can be drawn non-scaled or full size using formboard layout capabilities
- Automated generation of connector, wire and splice tables
- Built-in intelligent libraries for components and symbols
- Wire lengths calculations include terminal and connector add-on/knock-off values
- Integrates with VeSys 2.0 Design to import wires, connectors and splices and then back-annotate wire lengths
- Imports / Exports industry-standard harness exchange formats such as CHS 3D Harness (*.XML) and DSI
- Embedded Test Drive tutorial included

Mentor Graphics®

harness layout. Harness information such as wire lengths can be back annotated to the circuit diagram, further refining the electrical simulation accuracy.

Libraries are provided for components and symbols and these can be shared and exchanged with other companies



An integrated component library provides the foundation for automated part selection.

Productive

Once a harness is complete, the powerful VeSys 2.0 Harness reporting capabilities can be used to generate the documentation required for manufacturing. Because the reports are automatically generated directly from the design drawing, fast and accurate information is assured, significantly reducing production lead times and preventing manufacturing errors.

VeSys 2.0 interfaces to most major MCAD systems allowing accurate wire length information to be defined by the MCAD system

VeSys 2.0 is simple to use, ships with a detailed video tutorial and requires minimal training allowing companies to become productive quickly. An experienced support staff and an active user community forum provide immediate support and answers to new and experienced users alike.

Scalable

VeSys 2.0 shares a common software architecture with CHS, the comprehensive electrical and wire harness design software suite from Mentor Graphics. While VeSys 2.0 products are optimized for rapid deployment and a small IT footprint, CHS is designed for large multi-site enterprises.

A VeSys 2.0 installation upgrades readily to CHS with risk-free native data migration. CHS encompasses solutions for physical architecture optimization, systems design, systems integration and wiring design, electrical simulation and analysis, harness engineering, harness costing and documentation generation. Moreover, VeSys 2.0 and CHS are able to share common component, symbol and simulation model libraries.

For more information on CHS contact your Mentor Graphics representative or visit www.mentor.com/electrical.

Recommended Hardware Configurations for VeSys 2.0

Single-Tier (All components on one computer)

	Recommended (All)
Processor	Dual Core 2 GHz or higher
Memory	2 GB
Disk Space	25 GB
Operating System	Windows XP or Vista

Two-Tier (Server with clients)

	Recommended (Server)	Recommended (Client)
Processor	Dual Core 2 GHz or higher	Dual Core 2 GHz or higher
Memory	4 GB	1 GB
Disk Space	20 GB	500 MB
Operating System	Windows Server 2003 SP2 or higher	Windows XP or Vista

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