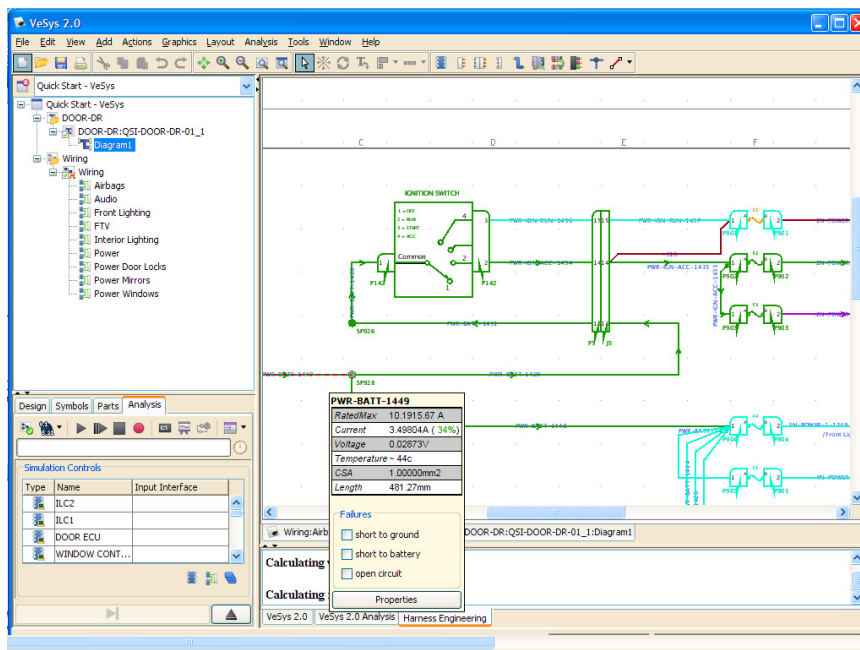


VeSys® 2.0 Design



Electrical simulation validates the design as it is created.

Intuitive

VeSys 2.0 Design provides a graphical environment for creating wiring diagrams. Full electrical design authoring is made easy via an intuitive user interface and electrically intelligent symbols. Built-in electrical intelligence automates many design tasks. For example, splices are automatically created when wires are joined and cross-references are automatically created when wires cross between sheets.

Furthermore, all entities have context sensitive menus giving the user the modification options appropriate for each different type of component. These and many other facilities make circuit editing a quick and simple.

Powerful

Built-in simulation helps designers to check and validate the design, as it is created, helping to find errors fast and early in the design phase. Simulation shows voltages and currents, highlighting problem areas such as short circuits and validating wire and fuse sizing.

Designs can be split into multiple sheets with automated cross-referencing and hyperlinks, helping users navigate the design. Diagram appearance – line weights, fonts, colors, symbols, borders, etc is fully customizable.

Libraries are provided for components, symbols and analysis models and these can be shared and exchanged with other companies.

VeSys 2.0 Design Highlights

- Rapid circuit design with intuitive user interface
- Simulation validates the electrical design as it is created
- Simulation highlights short circuits, volt-drop, fusing and wire sizing errors
- Automatic cross-referencing for multi-sheet parts and wires
- Customizable drawing styles
- Automated report generation
- Built-in intelligent libraries for components, symbols and simulation models
- Integration with MCAD systems for 3D harness routing and wire length back-annotation
- Embedded Test Drive tutorial included

Productive

VeSys 2.0 automatically generates reports for wires, connectors and devices used in the design. Diagram index-tables, device and wire index-tables with full sheet and zone referencing can be added to the drawings and these are updated automatically as changes are made.

VeSys 2.0 interfaces with most major MCAD systems allowing accurate wire length information to be defined by the MCAD system

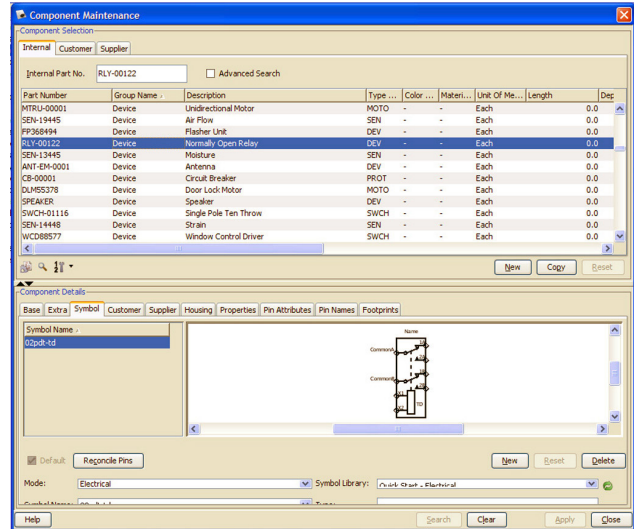
VeSys 2.0 is simple to use, ships with a detailed video tutorial and requires minimal training allowing companies to become productive quickly. Experienced support staff and an active user community forum provide immediate support and answers to new and experienced users alike.

Scalable

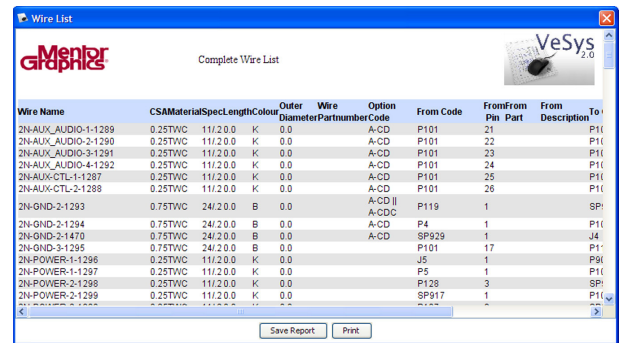
VeSys 2.0 shares a common software architecture with Mentor Graphics' CHS electrical and wire harness design suite. While VeSys 2.0 is optimized for rapid deployment and a small IT footprint, CHS is designed for larger enterprises.

A VeSys 2.0 installation upgrades readily to CHS with risk-free native data migration. CHS encompasses solutions for physical architecture optimization, systems design, systems integration and wiring design, electrical simulation and analysis, harness engineering, harness costing and documentation generation. Moreover, VeSys 2.0 and CHS are able to share common component, symbol and simulation model libraries.

For more information on CHS contact your Mentor Graphics representative or visit www.mentor.com/electrical.



VeSys 2.0 Design includes an integrated component library that speeds the creation of drawings.



VeSys automatically generates specialized reports and tables.

Single-Tier (All components on one computer)

Recommended Hardware Configurations for VeSys 2.0

	Recommended (All)
Processor	Dual Core 2 GHz or higher
Memory	2 GB
Disk Space	25 GB
Operating System	Windows XP or Vista

Two-Tier (Server with clients)

	Recommended (Server)	Recommended (Client)
Processor	Dual Core 2 GHz or higher	Dual Core 2 GHz or higher
Memory	4 GB	1 GB
Disk Space	20 GB	500 MB
Operating System	Windows Server 2003 SP2 or higher	Windows XP or Vista

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Corporate Headquarters
Mentor Graphics Corporation
8005 S.W. Boeckman Road
Wilsonville, Oregon 97070 USA
Phone: 503-685-7000
North American Support Center
Phone: 800-547-4303
Fax: 800-684-1795

Silicon Valley
Mentor Graphics Corporation
1001 Ridder Park Drive
San Jose, California 95131 USA
Phone: 408-436-1500
Fax: 408-436-1501

Europe
Mentor Graphics
Deutschland GmbH
Amulstrasse 201
80634 Munich
Germany
Phone: +49.89.57096.0
Fax: +49.89.57096.400

Pacific Rim
Mentor Graphics Taiwan
Room 1603, 16F,
International Trade Building
No. 333, Section 1, Keelung Road
Taipei, Taiwan, ROC
Phone: 886-2-27576020
Fax: 886-2-27576027

Japan
Mentor Graphics Japan Co., Ltd.
Gokuryama Hills
7-35, Kita-Shinagawa 4-chome
Shinagawa-Ku, Tokyo 140
Japan
Phone: 81-3-5488-3030
Fax: 81-3-5488-3031

